

SEARCHED	SERIALIZED
INDEXED	FILED

ISSUE CLASSIFICATION

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/841595	F	438	327	308	2816
APPLICANT'S	Keiichi Motoike				
TITLE	Semiconductor integrated circuit				

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)																																																			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)																																																		
INTERNATIONAL CLASSIFICATION																																																					
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TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent is subsequent to (date) has been disclaimed.	(Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. .	(Primary Examiner) (Date)				
<input type="checkbox"/> The terminal months of this patent have been disclaimed.	(Legal Instruments Examiner) (Date)			ISSUE FEE	
				Amount Due	Date Paid
				ISSUE BATCH NUMBER	

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